

Claims

[c1] An amplifier circuit suitable for amplifying an AM broadcast signal, the circuit comprising:

FET means for amplifying the AM broadcast signal and reducing a flicker noise level in the amplifier below an N-MOS transistor equivalent flicker noise; and a tuning circuit operatively connected between the FET means and an output node of the amplifier circuit.

[c2] The amplifier circuit of claim 1, wherein said FET means comprises two cascode-coupled P-MOS transistors.

[c3] The amplifier circuit of claim 1, wherein said FET means comprises two cascode-coupled P-MOS transistors which receive, respectively, the AM broadcast signal and an AGC voltage.

[c4] The amplifier circuit of claim 1, further comprising a DC-blocking capacitor, wherein the AM broadcast signal is coupled through the DC-blocking capacitor to the FET means at a gate of a P-MOS transistor contained therein.